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盛群產品使用須知
The using guideline of HOLTEK's products

壹、目的：

A、PURPOSE:

建立盛群產品使用手冊,以方便客戶正確的儲存及生產。

To edit the manual for HOLTEK's Products, and to ensure the well storage and manufacturing at customer side.

貳、適用範圍

B、SCOPE:

適用於所有盛群生產的產品。

Applying for all HOLTEK's products.

參、定義:無

C.DEFINITION: N/A

肆、權責:

D.RESPONSIBILITY:

一、品保處:

1. QRA Division:

制定/變更本份手冊

To edit and revise this document.

二、業務單位:

2. Sales Division:

收集/提供客戶需要的資訊

To collect and provide the information customer need.

伍、內容:

E. CONTENT:

一、盛群產品的儲存條件及使用年限

1. The storage condition and service life of Holtek's products.

(一).盛群出貨的產品包括晶圓,晶粒及構裝成品三類,其儲存條件(含外部包裝在內)為

1.1 The outgoing products in Holtek are wafers, chips, and package IC, all of the storage conditions with packing are following.

1.溫度: 22 ±5

1.1.1 temperature: 22 ±5

2.溼度: 50%RH±15%RH

1.1.2 humidity: 50%RH±15%RH

備註:盛群庫房的晶圓儲存條件因為儲存機台的問題,而有所調整,請參閱"5050-001 成品伴成品管理程序。

Note: due to the criteria of storage facility, the wafer storage condition is adjusted to certain extent in Holtek's warehouse.

(二).盛群產品的保存年限為

1.2The conservation life

1.晶圓,晶粒:製造後 1 年內。

1.2.1 Wafer and chip: Within one year after production.

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2.構裝成品: 製造後 2 年內。

1.2.2 Package IC: Within two years after production.

3.COF/TCP: 製造後 1 年內。

1.2.3 COF/TCP: Within one year after production.

(三).如果庫存產品超過保存年限,建議依下列方式處理

1.3 While over its storage date, please follow the suggested treatment to stocks

1.晶圓,晶粒: 重新以光學顯微鏡檢驗,來判定是否為良品,檢驗後的良品使用年限為 1 年。

1.3.1 Wafer and chip: Re-inspect by OM to check whether it is good. And storage date can be extended one more year if its quality can be confirmed.

2.構裝成品: 以 110 的烤箱烘烤 12 小時後重測,烘烤後的良品使用年限為 2 年。

1.3.2 Package IC: Bake it at 110 for 12 hrs, and then re-test by tester to check whether it is good. The storage date can be extended 2 more years.

3.COF/TCP: 重新以光學顯微鏡檢驗並進行重新測試,來判定是否為良品,檢驗後的良品使用年限為 1 年。

1.3.3 COF/TCP: Re-inspect by OM and re-test by tester to check whether it is good. And the storage date can extended one more year if its quality can be confirmed.

二、盛群產品的包裝及拆封後使用注意事項

2. The packing types and the items for attention after unpacking.

(一).盛群產品出貨的小箱包裝可分為三種

2.1 Three types of package with carton for Holtek's shipping.

1.乾燥包裝: 適用於 QFP/LQFP/TQFP/QFN 產品。

2.1.1 Dry packing: apply for QFP/LQFP/TQFP/QFN

2.真空包裝: 適用於晶粒及大部分的 SMD 產品。

2.1.2 Vacuum packing: apply for chips and SMD products.

3.無包裝: 適用於 DIP,TO 系列等插腳式產品及小部份 SMD 產品。

2.1.3 Without packing: apply for Through-hole products and some SMD products.

備註:盛群各封裝種類的包裝方式請參閱盛群網站"包裝/紙箱尺寸資訊"。

Note: The detail of packing type, please refer the information of "Packing and Carton dimension" by HOLTEK website.

(二).拆封後使用注意事項

2.2 Items for attention after unpacking

1.乾燥包裝: 拆封後 5 日使用完畢。

2.2.1 Dry packing: Use it within 5 days after unpacking.

2.真空包裝: 拆封後 5 日使用完畢。

2.2.2 Vacuum packing: Use it within 5 days after unpacking.

3.無包裝: 生產後(指封裝)2 年內使用完畢。

2.2.3 Without packing: Use it within 2 years after production

4.如果乾燥包裝產品和真空包裝產品未能在 5 日內使用完畢,建議以 110 的烤箱烘烤 12 小時後重新包裝儲存(晶粒/COF/TCP 僅需檢驗後上真空包裝,不需要烘烤)。

2.2.4 For dry or vacuum packing products, if it can't be used within 5 days. Please re-pack it after 110 baking by 12 hrs(Chip/COF/TCP do not need baking).

三、盛群的產品回溯

3. Product tracing:

(一).盛群內部: 我們可以由批號或是 DATE CODE 來查詢生產資料。

3.1 Internal tracing: the production information can be searched by lot number or date code.

(二).出貨產品: 可以由出貨單號及批號/DATE CODE 來回溯生產資料。

3.2 Shipped products: trace by shipping ticket, lot number and date code.

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備註:批號/DATE CODE 是附在出貨標籤上,而標籤是貼在小箱/捲帶上面。

Note: Lot number and date code shows on shipping label where it's stacked on carton or reel.

四、盛群產品 DATE CODE 編碼準則

4. The date-coding rule of products:

盛群 DATE CODE 總共由 11 個符號組成,其意義說明如下:

Holtek's date code is organized by 11 letters, the meanings are below:

假如某個 DATE CODE 為 **ABCDEFGHIJK**

If a date code is organized by **ABCDEFGHIJK**

	Meaning	Description
AB	Year	A6 or 06 : year 2006 A5 or 05 : year 2005
CD	Week	Week 01-53
E	Assembly house	
FGHI	Manufacturing number	
J	Plating type	"-" or blank : Sn-Pb product "%": Sn-Cu product "#": pure Sn product "G": pure Sn product (Green compound)
K	Design version	

五、盛群產品過錫爐條件

5. IR reflow condition for Holtek's products:

(一).表面黏著產品(遵照 JEDEC 規範 J-STD-020C)

5.1 SMD products (according to JEDEC criteria J-STD-020C):

Table 1- IR reflow condition

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Eutectic Assembly
Average Ramp-up rate	3 /second max.	3 /second max
Preheat	100 -150 60-120 seconds	150 -200 60-180seconds
Time maintained above	183 60-150seconds	217 60-150seconds
Peak Temperature	See table 2	See table 3
Time within 5C of actual peak Temp.	10-30 seconds	20-40 seconds
Ramp-down rate	6 /second max	6 /second max
Time 25C to Peak Temperature	8 minutes max	8 minutes max

Table 2 – peak temperature for Sn-Pb product

Package thickness	Volume mm ³ <350	Volume mm ³ 350
<2.5mm	240 +0/-5	225 +0/-5
2.5mm	225 +0/-5	225 +0/-5

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Table 3 – peak temperature for Pb-free product

Package thickness	Volume mm ³ <350	Volume mm ³ 350-2000	Volume mm ³ 2000
<1.6mm	260 +0	260 +0	260 +0
1.6mm – 2.5mm	260 +0	250 +0	245 +0
2.5mm	250 +0	245 +0	245 +0

(二) 插件式產品

Through-hole products

Solder technique simulation	Solder temperature()	Time(s)
Solder iron	350±10	4-5
Solder dip	260±5	10±1
Wave : Topside	260±5	20±1
Wave : Bottomside	260±5	10±1

六、EOS/ESD 的防護

6. Protection against the EOS and ESD

(一). EOS 的防護

6.1 Protection against the EOS:

1. 人員

6.1.1 People:

(1) 客戶需要建立自己的操作手冊。

6.1.1.1 Edit the acceptable SOP by customer self.

(2) 相關人員需要接受適當的訓練,並依據操作手冊進行生產及測試。

6.1.1.2 Take appropriate training, and follow the SOP to manufacturing and testing

(3) 人員需要配戴隔離靜電的裝備,以避免靜電破壞生產產品。

6.1.1.3 Anti-ESD accouterment wearing to avoid the ESD damaged the products.

2. 機器設備

6.1.2 Equipments:

(1) 要接地。

6.1.2.1 Equipments are properly grounded.

(2) 要有過電壓保護裝置。

6.1.2.2 Overload protection

(3) 適當的散熱處理。

6.1.2.3 Properly heat dissipation.

(4) 定期保養。

6.1.3.4 Regular maintenance

3. 正確地測試元件及電路板

6.1.3 Correct testing component and PCB

(1) 確認測試方式是否超過元件的最大規格(根據供應商提供的資料)。

6.1.3.1 To confirm the testing condition under the component's criteria .

(2) 檢查是否有過高的干擾電壓存在。

6.1.3.2 To ensure there is no higher noise.

(3) 確認電路板的元件是否有浮焊。

6.1.3.3 To confirm no soldering defect on PCB.

(4) 測試時需確認機器接頭是否鬆脫。

6.1.3.4 To ensure there are no loose connections.

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(二).ESD 的防護

6.2 Protection against the ESD

1.確認 IC 及電路板靜電防護電路的設計符合要求。

6.2.1 To confirm the ESD circuit is properly designed in the IC and PCB layout.

2.工作人員需要接受適當的靜電防護訓練,並在工作中配戴靜電環及穿著靜電衣鞋等裝備來隔絕靜電。

6.2.2 The operator should take the appropriate ESD protection training and wear the anti-static electricity ring, clothes and shoes.

3.架構一個有效的靜電防護環境

6.2.3 Outfit an effective workshop

(1)溼度控制在 50%RH±15%RH 內。

6.2.3.1 Humidity controlled under 50%RH±15%

(2)使用靜電防護的地板及桌墊。

6.2.3.2 Anti-static electricity floor and mat applying.

(3)生產機器設備需要有良好的接地設施。

6.2.3.3 Well grounding to equipment and machines.

(4)使用抗靜電材料的載具來搬運生產元件。

6.2.3.4 Anti-static electricity tools to carry the manufacturing components.

(5)必要時,使用離子風扇來消除局部區域的靜電。

6.2.3.5 Use the ionizer to remove the partial ESD if necessary.

陸、參考文件:無

F. Reference document: N/A

柒、資料保存:無

G. Data retention: N/A